

Please type a plus sign (+) inside this box ☐

Based on PTO/SB/08 a & b (05-03)
 Approved for use through 05/31/2003. OMB 0651-0031
 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
 Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Subsidiary for Form 1449A/PTO		Application Number	10/065,157
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing Date	10/12/02
		First Named Inventor	Steven Teig et al.
		Group Art Unit	2825
		Examiner Name	Tai, B.
Use as many sheets as necessary		Attorney Docket Number	SPLX:P0133
Street	1	of	9

U.S. PATENT APPLICATIONS						
Examiner Initials	Cite No.	U.S. Patent Application Serial Number	Applicant Docket Number	Name of Patentee or Applicant of Code Document	Date of Filing IMMEDIATE	Related Application Data if any
BT	1.	10/065,060	SPLX.P0072	Steven Teig	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	2.	10/065,160	SPLX.P0073	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	3.	10/065,005	SPLX.P0074	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	4.	10/065,047	SPLX.P0075	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	5.	10/061,641	SPLX.P0079	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	6.	10/065,094	SPLX.P0080	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	7.	10/076,121	SPLX.P0081	Steven Teig et al.	02-12-2002	Application with similar, but not identical, specification and drawing as the present application.
	8.	10/065,065	SPLX.P0105	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	9.	10/065,162	SPLX.P0106	Steven Teig	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	10.	10/260,584	CDN.P0037	Steven Teig	10-31-2002	
	11.	10/335,987	CDN.P0038	Steven Teig et al.	12-31-2002	
BT	12.	10/395,239	CDN.P0039	Steven Teig et al.	12-31-2002	

Examiner Signature	<i>B. Kint</i>	Date Completed	11/16/04
-----------------------	----------------	-------------------	----------

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 18 if possible. ⁶ Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

BEST AVAILABLE COPY

Please type a plus sign (+) inside this box ☐

Based on PTO/SB-08 a & b (05-09)
 Approved for use through 05/31/2003. OMB 0651-0031
 U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE
 Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Substitute for form 1449A-PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number 10/66,187
				Filing Date 1/31/2002
				First Named Inventor Steven Teig et al.
				Group Art Unit 2825
				Examiner Name Tai B.
Sheet 2	of 9	Attorney Docket Number SFLX: F0133		
U.S. PATENT APPLICATIONS				
BT	13.	10/335,005	CD/4:PU040	Steven Teig et al.
12/31/2002				

FOREIGN PATENT DOCUMENTS							
Examiner Initials	Ch. No.	Office	Foreign Patent Document Number	Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Good Document	Pages, Claiming Lines, Which Relate to Passages or Related Figures Appear	
BT	14.	JP	11-298580	10-29-1990	Matsumoto et al.	with English translation of Abstract	
	15.	JP	2000-082743	03-21-2000	Igarashi et al.	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	16.	JP	64-15947	01-19-1986	Ouchi	with English translation of Abstract	
	17.	JP	03-173471	07-26-1991	Tawada et al.	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	18.	JP	04-000677	01-05-1992	Fujikura et al.	with English translation of Abstract	
	19.	JP	05-102305	04-23-1993	Seto	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
BT	20.	JP	05-243370	08-21-1993	Kubota	with Japanese Patent Office's English translation of Abstract; and with English	✓

Examiner Signature <i>B. H. Lee</i>	Date Considered 11/16/04
--	-----------------------------

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. * Applicant's unique citation designation number (optional). * See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. * Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). * For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. * Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. * Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37CFR 1.07 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 38 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-6199 and select option 2.

BEST AVAILABLE COPY

Please type a plus sign (+) inside this box ☐

Based on PTO/SB/08 a & b (05-03)
Approved for use through 05/31/2003. OMB 0651-0031
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1996, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Substitute for form 1449A-PTO				Application Number	10/660,187
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Filing Date	1/31/2002
				First Name of Inventor	Steven Teig et al.
				Group Art Unit	2825
				Examiner Name	Tai B.
Sheet	2	of	9	Attorney Docket Number	SPLX: P0133

U.S. PATENT APPLICATIONS

13.	10/335,085	CDN.P0040	Steven Teig et al.	12/31-2002
-----	------------	-----------	--------------------	------------

FOREIGN PATENT DOCUMENTS

Examiner's Initials	Ch. No.	Office	Foreign Patent Document Number	Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Whole Figures, Passages, or Referred Figures Appear	✓
	14.	JP	11-296580	10-29-1999	Matsumoto et al.	With English translation of Abstract.	
	15.	JP	2000-082743	03-21-2000	Igarashi et al.	With Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	16.	JP	04-15947	01-19-1986	Ouchi	With English translation of Abstract.	
	17.	JP	03-173471	07-26-1991	Tawada et al.	With Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	18.	JP	04-006677	01-06-1992	Fujimura et al.	With English translation of Abstract.	
	19.	JP	05-102305	04-23-1993	Sato	With Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	20.	JP	05-243370	09-21-1993	Kubota	With Japanese Patent Office's English translation of Abstract; and with English.	✓

BEST AVAILABLE COPY

Examiner Signature	Date Considered
--------------------	-----------------

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. * Applicant's unique citation designation number (optional). * See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. * Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). * For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. * Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 18 if possible. * Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37CFR 1.87 and 1.88. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1460, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1460, Alexandria, VA 22313-1460.

If you need assistance in completing the form, call 1-800-PTO-0199 and select option 2.

Please type a plus sign (+) inside this box ☐

Based on PTO/SB/08 a & b (05-09)
 Approved for use through 05/31/2003. OMB 0651-0031
 U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE
 Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Substitute for form 1449A-PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number 10/66,187
Filing Date 1/5/2002				
First Named Inventor Steven Teig et al.				
Group Art Unit 2825				
Examiner Name Tai B.				
Attorney Docket Number SFLX:F0133				
Sheet	2	of	9	
U.S. PATENT APPLICATIONS				
13.	10/335,005	CDN:PU040	Steven Teig et al.	12/31-2002

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Ch. No.	Foreign Patent Document Number	Date of Publication	Name of Patentee or Applicant of Good Document	Pages, Columns, Lines, Which Appropriate Passages or Relevant Figures Appear	
	14.	JP 11-298580	10-29-1999	Matsumoto et al.	with English translation of Abstract	
	15.	JP 2000-082743	03-21-2000	Igarashi et al.	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	16.	JP 64-15947	01-19-1989	Ouchi	with English translation of Abstract	
	17.	JP 03-173471	07-26-1991	Tanaka et al.	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	18.	JP 04-009677	01-08-1992	Fujizawa et al.	with English translation of Abstract	
	19.	JP 05-102305	04-23-1993	Sato	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	20.	JP 05-243379	09-21-1993	Kubota	with Japanese Patent Office's English translation of Abstract; and with English	✓

Examiner Signature	Date Considered
--------------------	-----------------

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. * Applicant's unique citation designation number (optional). * See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. * Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). * For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. * Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. * Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37CFR 1.87 and 1.88. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 38 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1480, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1480, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-0199 and select option 2.

BEST AVAILABLE COPY

Please type a plus sign (+) inside this box



Based on PTO/SB/08 a & b (05-03)
 Approved for use through 05/31/2003. OMB 0851-0031
 U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE
 Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of
 information unless it contains a valid OMB control number

Substitute for form 1449A/PTO:				Application Number		10/060,167	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Filing Date		1/31/2002	
				First Named Inventor		Steven T. H. et al.	
				Group Art Unit		2825	
				Examiner Name		Tel. 6.	
Sheet		3 of 9		Attorney Check Number		SPLEX P0133	
FOREIGN PATENT DOCUMENTS							
						translation of the application with Japanese Patent Office's English translation of Abstract and with English translation of the application.	
BT	21.	JP	07-086407	03-31-1995	Miura		
BS	22.	JP	09-152279	06-20-1997	Yoshida		

NON PATENT LITERATURE DOCUMENTS							
Examiner's Initials	Citation	include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the book (if applicable), journal, serial, symposium, date, and volume (if applicable), and the name of the publisher (if applicable).					
BT	23.	Chen, H.P. et al., A Better Algorithm for Rubber-Band Equivalent Transformation for Planar VLSI Layouts, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 15, No. 2, February 1996, pp. 217-227.					
	24.	Chip Model with Wiring Cost Map, August 1993, IBM Technical Disclosure Bulletin, vol. 26, issue 5A, pp. 929-933.					
	25.	Dayan, T. et al., Layer Assignment for Rubber Band Routing, VLSI-CR-94-04, January 20, 1993.					
	26.	Dayan, T., Rubber-Band Based Topological Router, A Dissertation, UC Santa Cruz, June 1997.					
	27.	Doed, P. et al., A Two-Dimensional Topological Compactor with Octagonal Geometry, 28th ACM/IEEE Design Automation Conference, pp. 727-731, July 1991.					
	28.	Fujiwara, K. et al., Homotopic Snaps Information.					
BS	29.	Hama, T. et al., Curvilinear Detailed Routing Algorithm and its Extension to Wire Spreading and Wire Fattening.					

Examiner Signature	<i>B. H. Lat</i>	Date Considered	11/16/04
--------------------	------------------	-----------------	----------

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. * Applicant's unique citation designation number (optional). * See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. * Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). * For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. * Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 18 if possible. * Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 38 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing this form, call 1-800-PTO-0199 and select option 2.

BEST AVAILABLE COPY

Please type a plus sign (+) inside this box.



Based on PTO/SB/08 a & b (05-03)
 Approved for use through 06/31/2003. OMB 0651-0031
 U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE
 Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of
 information unless it contains a valid OMB control number

Speculate for form 1540A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Application Number 10/056,187
			Filing Date 1/31/2002
			First Named Inventor Steven Teig et al.
			Group Art Unit 2825
			Examiner Name Tai B.
			Attorney/Agent/Member SPLX.P0133
Sheet	4	of	9
NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS); title of the article (when appropriate); title of the book, magazine, journal, serial, symposium, etc.; date, serial volume, page number(s) (month, day, year); country where published	
PT	30.	Flame, T. et al., Topological Routing Path Search Algorithm with Incremental Routability Test, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 19, No. 2, February 1999, pp. 142-150.	
	31.	Kobayashi, K. et al., A New Interactive Analog Layout Methodology based on Rubber-Band Routing, UESC-CRL-95-12, June 13, 1995.	
	32.	Ullm, A. et al., A Fast Algorithm To Test Planar Topological Routability, Technical Report 94-012, pp. 1-16.	
	33.	Gu, Y., Dynamic Constrained Delaunay Triangulation and Application to Multichip Module Layout, A Thesis for Master of Science, UC San Diego, December 1991.	
	34.	Ataley, F.A.L., Testing Homotopic Routability Under Polygonal Wiring Rules, Algorithmica 1996, 15:1-18.	
	35.	McDon, P. B. et al., An Efficient Sequential Quadratic Programming Formulation of Optimal Wire Spacing for Cross-Talk Noise Avoidance Routing, UESC-CRL-99-05, March 16, 1999.	
	36.	RM71091316, Use of Relatively Diagonal And Rectangular Wiring Planes in Multilayer Packages, September 1971, IBM Technical Disclosure Bulletin, Vol. No. 14, Issue No. 4, pp. 1316-1317.	
	37.	Stangelaars, D. et al., Geometric Transformations for a Rubber-Band Sketch, A Thesis for a Master of Science in Computer Engineering, UCSC, September 1992.	
	38.	Stangelaars, D. et al., Surf: A Rubber-Band Routing System for Multichip Modules, pp 19-26, 1993.	
	39.	Su, J. et al., Post-Route Optimization for Improved Yield Using Rubber-Band Wiring Model, 1997 International Conference on Computer-Aided Design, pp 700-706, November 1997.	
	40.	Wei-King Dai, W. et al., Routability of a Rubber-Band Sketch, 28th ACM/IEEE Design Automation Conference, 1991, pp. 45-55.	
	41.	Xing, Z. et al., A Minimum Cost Path Search Algorithm Through Tile Obstacles, slide presentation.	
	42.	Xing, Z. et al., Shortest Path Search Using Tiles and Piecewise-Linear Cost Propagation, (IEEE) 2002, pp.145-158.	
	43.	Ali, A More Efficient Distance Vector Routing Algorithm, UESC-CRL-99-16, March 1997.	
PT	44.	Yu, M.-F. et al., Fast and Incremental Routability Check of a Topological Routing Using a Cut-Based Encoding, UESC-CRL-97-07, April 14, 1997.	
Examiner Signature			Date Considered 4/16/04

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. * Applicant's unique citation designation number (optional). * See Kind Code of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. * Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). * For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. * Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 15 if possible. * Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37CFR 1.07 and 1.08. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9189 and select option 2.

BEST AVAILABLE COPY

Based on PTO/SB/08 a & b (05-03)

Approved for use through 05/31/2003. OMB 0851-0031

Please type a plus sign (+) inside this box U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Submission for form 1449A/PTO		Application Number 10055187
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing Date 1/31/2003
(use as many sheets as necessary)		First Named Inventor Steven Tsai et al.
Sheet 5 of 5		Group Ref. Unit 2825
Examiner Name Tol. B.		Agency Docket Number SPLX.P0133
NON PATENT LITERATURE DOCUMENTS		
45.	Yu, M.-F. et al., Interchangeable Pin Routing with Application to Package Layout, UCSC-CRL-95-10, April 25, 1995.	
46.	Yu, M.-F. et al., Pin Assignment and Routing on a Single-Layer Pin Grid Array, UCSC-CRL-95-15, February 24, 1995.	
47.	Yu, M.-F. et al., Planar Interchangeable 2-Terminal Routing, UCSC-CRL-95-49, October 10, 1995.	
48.	Yu, M.-F. et al., Single-Layer Fanout Routing and Routability Analysis for Ball Grid Arrays, UCSC-CRL-95-18, April 25, 1995.	
49.	Knob, R. et al., Faster Algorithms for the Shortest Path Problem, Journal of the Association for Computing Machinery, vol. 37, No. 2, April 1990, pp. 213-223.	
50.	Alexander, M. et al., Performance-Oriented Placement and Routing for non-programmable gate arrays, Proceedings of the European Design Automation Conference, pages 80-85, 1999.	
51.	Alexander, M. et al., Placement and Routing for Performance-Oriented FPGA Layout, VLSI Design, Vol. 7, No. 1, 1998.	
52.	Andou, H. et al., Automatic Routing Algorithm for VLSI, 22 nd Design Automation Conference, 1995, pp. 783-788.	
53.	Baggio, J. et al., Internal, External, and Mixed Visibility Edges of Polygons.	
54.	Barger, B. et al., Nearly Optimal Algorithms and Bounds for Multilayer Channel Routing, Journal of the Association for Computing Machinery, pp. 500-542, March 1995.	
55.	Brady, L. et al., Channel Routing on a 60° Grid, expanded abstract, pp. 826-831.	
56.	Carothers, K., A Method of Measuring Net Routability for MCMA's General Area Routing Programs, 1999, pp. 186-192.	
57.	Chen, D.-S. et al., A Wire-Length Minimization Algorithm for Single-Layer Layouts.	
58.	Chen et al., Optimal Algorithms for Bubble Sort Based Non-Manhattan Channel Routing, May 1994, Comput-Aided Design of Integrated Circuits and Systems, IEEE Transactions Volume, 13, Issues, pp. 603-609.	
59.	Chen, H., Routing L-Shaped Channels in Nonsliding-Structure Placement, 24 th ACM-IEEE Design Automation Conference, pp. 152-145, 1987.	

BEST AVAILABLE COPY

Examiner Signature 	Date Considered 11/16/04
---	-----------------------------

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. * Applicant's unique citation designation number (optional). * See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. * Enter Office that issued the document, by the two-letter code (WIPO Standard ST.31). * For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. * Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. * Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37CFR 1.97 and 1.85. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

Please type a plus sign (+) inside this box ☐

Based on PTO/SB/08 a & b (05-03)
 Approved for use through 05/31/2003. OMB 0851-0031
 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
 Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Submission for form 1449a/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Application Number 10/068,137
			Filing Date 1/31/2002
			First Named Inventor Steven Teig et al.
			Group Art Unit 2825
			Examiner Name Tai, B.
			Attorney/Agent Number SPLX.P0153
Sheet	6	of	9
NON-PATENT LITERATURE DOCUMENTS			
60.	Chen, H. et al., Physical Planning of On-Chip Interconnect Architectures, 2002, IEEE, International Conference, pp. 30-35.		
61.	Chen, S.-S. et al., A New Approach to the Ball Grid Array Package Routing, IEICE Trans. Fundamentals, Vol. E82-A, No. 11, November, 1999, pp. 2598-2609.		
62.	Cheng, K. et al., Manhattan or Non-Manhattan? A Study of Alternative VLSI Routing Architectures, pp. 47-62, 1990.		
63.	Cheng, K., Sticker Problem in Octilinear Routing Model: A Thesis Submitted for the Degree of Master of Science, National University Singapore, 1995, pp. 1-122.		
64.	Chiang, C. et al., Wirability of Knock-Knee Layouts with 45° Wires, IEEE Transactions on Circuits and Systems, Vol. 38, Issue 6, pp. 613-624, June 1991.		
65.	Cong, J. et al., Efficient Heuristics for the Minimum Shortest Path Steiner Arborisation Problem with Applications to VLSI Physical Design, Cadence Design Systems, pp. 63-69.		
66.	Cong, J. et al., Multilevel Approach to Full Chip Gridless Routing, 11/2001, IEEE, pp. 395-403.		
67.	Cong, J. et al., Performance Driven Multi-Layer General Routing for PCB/MCM Designs, UCLA Computer Science Department, 1995, pp. 358-361.		
68.	Das, S. et al., Channel Routing in Manhattan-Diagonal Model, 9 th International Conference on VLSI Design, January 1996, pp. 43-48.		
69.	Das, S. et al., Routing of L-Shaped Channels, Switchboxes and Staircases in Manhattan-Diagonal Model, pp. 85-70, January 1993.		
70.	Embrey, R. et al., Near-Optimal n-Layer Channel Routing, 23 rd Design Automation Conference, 1986, pp. 708-714.		
71.	Finch, A.C. et al., A Method for Gridless Routing of Printed Circuit Boards, 22 nd Design Automation Conference, 1985 ACM, pp. 509-515.		
72.	Geo, S. et al., Channel Routing of Multiterminal Nets, Journal of the Association for Computing Machinery, Vol. 41, No. 4, July 1994, pp. 791-818.		
73.	Geo, T. et al., Minimum Crosstalk Channel Routing, pp. 692-695, 1993 IEEE.		
74.	Geo, T. et al., Minimum Crosstalk Switchbox Routing, pp. 610-615, 1994 ACM.		

BEST AVAILABLE COPY

Examiner Signature	<i>M. H. Lee</i>	Date Considered	11/16/04
--------------------	------------------	-----------------	----------

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. * Applicant's unique citation designation number (optional). * See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. * Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). * For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. * Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 10 if possible. * Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

Please type a plus sign (+) inside this box ☐

Based on PTO/SB/08 a & b (05-03)
 Approved for use through 05/31/2003. OMB 0851-0031
 U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE
 Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Substituted for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Application Number 11/066,187
			Filing Date 1/5/2002
			First Named Inventor Steven Teige et al.
			Group And Unit 2825
			Examiner Name Tai, B.
Sheet 7 of 9			Attorney/Agent Number SPLX.FM133
NON PATENT LITERATURE DOCUMENTS:			
75.	Gonzalez, T. et al., A Linear Time Algorithm for Optimal Routing, Journal of the Association for Computing Machinery, vol. 35, No. 4, October 1988, pp.810-831.		
76.	Gibas, L. et al., Optimal Shortest Path Queries in a Simple Polygon, 1987 ACM, pp.50-63.		
77.	Hachtel, G.D. et al., Linear Complexity Algorithms for Hierarchical Routing, 1/89, IEEE, pp.64-80		
78.	Hershberger, J., Efficient Breakout Routing in Printed Circuit Boards, Computational Geometry, 1987, ACM, pp. 460-462.		
79.	Hershberger, J., Finding the Visibility Graph of a Simple Polygon in Time Proportional to its Size, Preliminary Version, 1987 ACM, pp. 11-20.		
80.	Hightower, D., A Solution to Line-Routing Problems on the Continuous Plane, Bell Laboratories, Inc., pp. 11-34.		
81.	Iso, H. et al., Efficient Routability Checking for Global Wires in Planar Layouts, IEEE Trans. Fundamentals, Vol. E66-A, No. 10 October 1987, pp. 1579-1582.		
82.	Koop, K. et al., An Efficient Multilayer MCM Router Based on Four-Via Routing, 30 th ACM/IEEE Design Automation Conference, 1993, pp. 590-593.		
83.	Ladaga, L. et al., Resistance Extraction Using a Routing Algorithm, 30 th ACM/IEEE Design Automation Conference, 1993, pp. 38-42.		
84.	Leach, G., Improving Worst-case Optimal Delaunay Triangulation Algorithms, Department of Computer Science, June 15, 1992, pp. 1-7.		
85.	Leiserson, C. et al., Algorithms for Routing and Testing Routability of Planar VLSI Layouts, pp. 60-78; May 1985.		
86.	Lillis, J. et al., New Performance Driven Routing Techniques With Explicit Area/Delay Tradeoff and Simultaneous Wire Sizing, 33 rd Design Automation Conference, 1996.		
87.	Lipsht, W. et al., A Unified Approach to Layout Wireability, Mathematical Systems Theory, 1987, pp. 189-203.		
88.	Lodi, E. et al., A 2d Channel Router for the Diagonal Model, pp. 111-125, April 1991.		
89.	Lodi, E. et al., A Preliminary Study of a Diagonal Channel-Routing Model, Algorithmica, 1989, pp.565-597.		

Examiner Signature	<i>B. H.</i>	Date Considered	11/16/04
--------------------	--------------	-----------------	----------

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 808. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37CFR 1.97 and 1.98. This information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

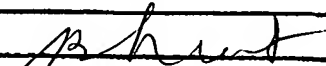
If you need assistance in completing the form, call 1-800-PTO-0189 and select option 2.

BEST AVAILABLE COPY

Please type a plus sign (+) inside this box ☐

Based on PTO/SB/08 a & b (05-03)
 Approved for use through 05/31/2003. OMB 0851-0031
 U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE
 Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Substitute for form 1449A/PTO		Application Number	10486,187
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Date	1/31/2002
		First Named Inventor	Steven Tsig et al.
		Group Am. Unit	2825
		Examiner Name	Tat. B.
Sheet	8	of	9
		Attorney Docket Number	SPLX:P0133
NON PATENT LITERATURE DOCUMENTS			
90.	Lodi, E. et al., Lecture Notes In Computer Science, A 4d-Channel router for a two-layer diagonal model, pp. 404-476, July 1988.		
91.	Lodi, E. et al., Routing in Times Square Mode, pp. 41-48, June 1990.		
92.	Lodi, E. et al., Routing Multiterminal Nets in a Diagonal Model, pp. 899-902, 1988.		
93.	Murcoka, T. et al., Simplified Routing Procedure for a CAD-Verified FPGA, IEEE Trans. Fundamentals, Vol. E82-A, No. 11 November 1999, pp. 2440-2447.		
94.	Nackerlo, N. et al., Via Minimization for Gridless Layouts, 24 th ACM/IEEE Design Automation Conference, 1987, pp. 159-165.		
95.	Nam, G. et al., Satisfiability-Based Layout Remission: Detailed Routing of Complex FPGAs Via Search-Based Heuristics SAT, 1999, pp. 167-175.		
96.	Nestor, J. A New Look at Hardware Maze Routing, Proceedings of the 12 th ACM Symposium on Great Lakes Symposium on VLSI, pp. 142-147, April 2002.		
97.	Ng, C., A "Gridless" Variable-Width Channel Router for Macro Cell Design, 24 th ACM/IEEE Design Automation Conference, 1987, pp. 833-836.		
98.	Olaveri, A.G. et al., On the Minimum Size of Visibility Graphs.		
99.	Overkne, G., EDA Underwriter 2 Finding Space in a Multi-Layer Board, Electronic Engineering, Morgan-Grampian LTD, vol. 87, no. 819, pp. 29-30.		
100.	Pocchicci, M., Computing the Visibility Graph via Pseudo-Triangulations, 11 th Computational Geometry, Vancouver, Canada, 1995 ACM, pp. 246-257.		
101.	Powers, K. et al., The 60 th Grid: Routing Channels in Width $d/\sqrt{3}$, VLSI, 1991, Proceedings, First Great Lakes Symposium on Kalamazoo, MI, USA, pp. 214-291, March 1991.		
102.	Reyle, J. et al., Geometric Compaction in One Dimension for Channel Routing, 24 th ACM/IEEE Design Automation Conference, 1987, pp. 140-145.		
103.	Schiele, W. et al., A Gridless Router for Industrial Design Rule, 27 th ACM/IEEE Design Automation Conference, pp. 626-631, 1990.		
104.	Sakiyama, Y. et al., Timing-Oriented Routers for PCB Layout Design of High-Performance Computers, International Conference on Computer Aided Design, pp. 392-395, November 1991.		

Examiner Signature		Date Considered	11/16/04
--------------------	---	-----------------	----------

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 608. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 18 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37CFR 1.87 and 1.89. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

BEST AVAILABLE COPY

Please type a plus sign (+) inside this box ☐

Based on PTO/SB/08 a & b (05-03)
 Approved for use through 06/31/2003. OMB 0651-0031
 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
 Under the Paperwork Reduction Act of 1996, no persons are required to respond to a collection of information unless it contains a valid OMB control number

Substituted for form 1049A/PTO		Application Number	10/000,157
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Date	1/31/2002
		First Named Inventor	Steven Teig et al.
		Group Art Unit	2925
		Examiner Name	Tal, D.
Sheet	9 of 9	Attorney Docket Number	SPLX/PO433
NON PATENT LITERATURE DOCUMENTS			
105	Sankub, J. et al, Maze Router Without a Grid Map, IEEE, 1992, pp. 382-385.		
106	Takasima, Y. et al, Routability of FPGAs with Extremal Switch-Block Structures, IEEE Trans. Fundamentals, vol. E41-A, No. 5, May 1996, pp. 850-856.		
107	Teig, S, The X-Architecture: Not your Father's Diagonal Wiring, International Workshop on System Level Interconnect Prediction, pp. 33-37, April 2002.		
108	Thakur, S. et al, Algorithms for a Switch Module Routing Problem, 1994, pp. 265-270.		
109	Tiptone, D. et al, HERO: Hierarchical EMC-constrained routing, 1992, IEEE pp. 466-472.		
110	Tolks, J, Techniques for Wiring in Non-Square Grids, pp. 66-69, May 1989.		
111	Urrutia, J., On the Number of Internal and External Visibility Edges of Polygons, Department of CS, University of Ottawa, Ott, Canada, February 11, 1987.		
112	Wang, D., Novel Routing Schemes for IC Layout, Part I: Two-Layer Channel Routing, 20 th ACM/IEEE Automation Conference, 1991, pp. 49-53.		
113	Yan et al., Three-Layer Bubble-Sorting-Based Non-Manhattan Channel Routing, ACM Transactions on Design Automation of Electronic Systems, Vol. 5, No. 3, July 2000, pp. 726-734.		
114	Zhou, H. et al., An Optimal Algorithm for River Routing with Crosstalk Constraints, 1995		
115	Zhou, H. et al., Optimal River Routing with Crosstalk Constraints, ACM Transactions on Design Automation of Electronic Systems, vol. 5, No. 3, July 1998, pp. 498-514.		

BEST AVAILABLE COPY

Examiner Signature	<i>B. H. Smith</i>	Date Considered	07/16/04
--------------------	--------------------	-----------------	----------

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 18 if possible. ⁶ Applicant is to place a check mark here if English language translation is attached.

This collection of information is required by 37CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing the burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22312-1450.

If you need assistance in completing the form, call 1-800-PTO-9189 and select option 2.

RECEIVED
CENTRAL FAX CENTER
Page 1 of 4
SEP 23 2004

Information Disclosure Statement

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention:	Method and apparatus for routing a set of nets						
Application Number:	10/066187						
Confirmation Number:	5277						
First Named Applicant:	Steven Teig						
Attorney Docket Number:	SPLX.P0133						
Art Unit:	2825						
Examiner:	Binh C. Tai						
Search string:	(4615011 or 4782193 or 5633479 or 5634093 or 5635736 or 6128767 or 6219823 or 6226560 or 6262487 or 6295634 or 6436804 or 6490713 or 6546540 or 6645842 or 4673966 or 4855929 or 5360948 or 5375069 or 5532934 or 5578840 or 5618744 or 5636125 or 5637920 or 5650653 or 5657242 or 5663691 or 5717600 or 5723908 or 5742086 or 5757089 or 5757656 or 5777360 or 5811863 or 5822214 or 5838583 or 5859449 or 5889329 or 5889677 or 5898597 or 5914887 or 5973376 or 5980093 or 6035108 or 6038383 or 6058254 or 6067469 or 6068662 or 6088519 or 6111756 or 6123736 or 20020104061 or 20020100009 or 20020107711 or 20020182844 or 20030005399 or 20030188281 or 20010003843 or 20020174413 or 20030025205 or 20030121017).pn.						
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
Init	Cite No.	Patent No.	Date	Patentee	Kind	Class	Subclass
PR	1	4615011	1986-09-30	Linsker			
PR	2	4782193	1988-11-01	Linsker			

BEST AVAILABLE COPY

file://C:\Program%20Files\USPTO\cPAVE\efiling\SPLX.P0133a\SPLX.P0133a-usidst.xml 4/21/2004

Information Disclosure Statement

Page 2 of 4

3	5633479	1997-05-27	Hirano	
4	5634093	1997-05-27	Ashida et al.	
5	5635736	1997-06-03	Funaki et al.	
6	6128767	2000-10-03	Chapman	
7	6219823	2001-04-17	Hama et al.	B1
8	6226560	2001-05-01	Hama et al.	B1
9	6262487	2001-07-17	Igarashi et al.	B1
10	6295634	2001-09-25	Matsumoto	B1
11	6436804	2002-08-20	Igarashi et al.	B2
12	6490713	2002-12-03	Matsumoto	B2
13	6546540	2003-04-08	Igarashi et al.	B1
14	6645842	2003-11-11	Igarashi et al.	B2
15	4673966	1967-06-16	Shimoyama	
16	4855929	1969-08-08	Nakajima	
17	5360948	1994-11-01	Thornberg	
18	5375069	1994-12-20	Sato et al.	
19	5532934	1996-07-02	Rostoker et al.	
20	5578840	1996-11-26	Scepanovic et al.	
21	5613744	1997-04-08	Suzuki et al.	
22	5636125	1997-06-03	Rostoker et al.	
23	5637920	1997-06-10	Loe	
24	5650653	1997-07-22	Rostoker et al.	
25	5657242	1997-08-12	Sekiyama et al.	
26	5663891	1997-09-02	Bamji et al.	
27	5717600	1998-02-10	Ishizuka	
28	5723908	1998-03-03	Fuchida et al.	
29	5742086	1998-04-21	Rostoker et al.	
30	5757089	1998-05-26	Ishizuka	
31	5757656	1998-05-26	Hershberger et al.	
32	5777360	1998-07-07	Rostoker et al.	
33	5811863	1998-09-22	Rostoker et al.	
34	5822214	1998-10-13	Rostoker et al.	

BEST AVAILABLE COPY

File://C:\Program%20Files\USPTO\oPAVE\efiling\SPLX.P0133a\SPLX.P0133a-usidst.xml 4/21/2004

Information Disclosure Statement

Page 3 of 4

<input checked="" type="checkbox"/>	35	5838583	1998-11-17	Varadarajan et al.
<input type="checkbox"/>	36	5859449	1999-01-12	Kobayashi et al.
<input type="checkbox"/>	37	5889329	1999-03-30	Rostoker et al.
<input type="checkbox"/>	38	5889577	1999-03-30	Yasuda et al.
<input type="checkbox"/>	39	5898597	1999-04-27	Scepanovic et al.
<input type="checkbox"/>	40	5914887	1999-06-22	Scepanovic et al.
<input type="checkbox"/>	41	5973376	1999-10-26	Rostoker et al.
<input type="checkbox"/>	42	5980093	1999-11-09	Jones et al.
<input type="checkbox"/>	43	6035108	2000-03-07	Kikuchi
<input type="checkbox"/>	44	6038383	2000-03-14	Young et al.
<input type="checkbox"/>	45	6038254	2000-05-02	Scepanovic et al.
<input type="checkbox"/>	46	6067409	2000-05-23	Scepanovic et al.
<input type="checkbox"/>	47	6068662	2000-05-30	Scepanovic et al.
<input type="checkbox"/>	48	6088519	2000-07-11	Koford
<input type="checkbox"/>	49	6111756	2000-08-29	Moresco
<input checked="" type="checkbox"/>	50	6123736	2000-09-28	Pavlic et al.

US Published Applications:

Note: Applicant is not required to submit a paper copy of cited US Published Applications

Init	Cite No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
<input checked="" type="checkbox"/>	1	20020104001	2001-11-30	Xing et al.	A1		
<input type="checkbox"/>	2	20020100009	2002-07-25	Xing et al.	A1		
<input type="checkbox"/>	3	20020107711	2002-08-08	Xing et al.	A1		
<input type="checkbox"/>	4	20020182844	2002-12-05	Igarashi et al.	A1		
<input type="checkbox"/>	5	20030005399	2003-01-02	Igarashi et al.	A1		
<input type="checkbox"/>	6	20030188281	2003-10-02	Xing	A1		
<input type="checkbox"/>	7	20010003845	2001-06-14	Scepanovic et al.	A1		
<input type="checkbox"/>	8	20020174413	2002-11-21	Tanaka	A1		
<input type="checkbox"/>	9	20030025205	2003-02-06	Shively	A1		
<input checked="" type="checkbox"/>	10	20030121017	2003-06-26	Andreev et al.	A1		

Signature

B Nat

11/16/04

BEST AVAILABLE COPY

file://C:\Program%20Files\USPTO\efiling\SPLX.P0133a\SPLX.P0133a-usidst.xml 4/21/2004

Information Disclosure Statement

Page 4 of 4

Examiner Name	Date
<i>R. Hart</i>	11/6/04

BEST AVAILABLE COPY

file://C:\Program%20Files\USPTO\ePAVE\efiling\SPLX.P0133a\SPLX.P0133a-usids.xml 4/21/2004